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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:  
Gen KOSHITA

Appn. No.: 10/813,761 : Art Unit: 2818  
Filed: March 30, 2004 : Examiner: To Be Assigned  
For: **BOOSTING CIRCUIT AND  
SEMICONDUCTOR DEVICE USING  
THE SAME** : Docket No.: KPM-02101

**Certificate of Mailing**

I hereby certify that the foregoing documents are being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this date of July 8, 2004.

*Bonny Rogers*

Name: Bonny Rogers

**REQUEST TO CORRECT FILING RECEIPT**

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Sir:

Please make the following correction on the Filing Receipt for the above-captioned application:

Under "Foreign Applications" please insert:

JAPAN 2004-043614 02/19/2004

A copy of the Filing Receipt with the change noted thereon is submitted herewith. Should there be any questions concerning this request, the Examiner is invited to contact the undersigned at (617) 248-4038.

Respectfully submitted,  
CHOATE, HALL & STEWART

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July 8, 2004  
Date

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APPL NO.	FILED OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/813,761	03/30/2004	2818	942	KPM-02101	9	20	5



Patent Group  
 Choate, Hall & Stewart  
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 Boston, MA 02109-2804

CONFIRMATION NO. 5275

FILING RECEIPT



\*OC000000012968295\*

Date Mailed: 06/17/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Gen Koshita, Tokyo, JAPAN;

Assignment For Published Patent Application

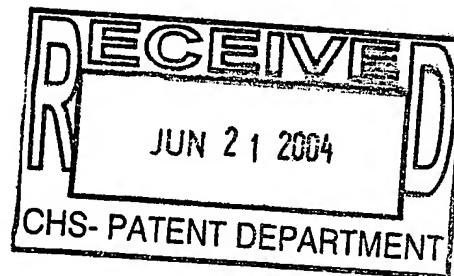
Elpida Memory, Inc.;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2004-043614 02/19/2004

If Required, Foreign Filing License Granted: 06/16/2004



Projected Publication Date: 10/06/2005

Non-Publication Request: No

Early Publication Request: No

Title

Boosting circuit and semiconductor device using the same